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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/654,618	09/04/2003	Young-Chan Kim	1293.1851	5000
21171	7590	03/30/2009	EXAMINER	
STAAS & HALSEY LLP SUITE 700 1201 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005			SHERMAN, STEPHEN G	
		ART UNIT	PAPER NUMBER	
		2629		
		MAIL DATE		DELIVERY MODE
		03/30/2009		PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/654,618	KIM ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	STEPHEN G. SHERMAN	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 21 January 2009.  
 2a) This action is **FINAL**.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-2,3-7,9-17,20-31,34-44,46-53 and 55-58 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1-2,3-7,9-17,20-31,34-44,46-53 and 55-58 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on 04 September 2003 is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____ .
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____ .	5) <input type="checkbox"/> Notice of Informal Patent Application
	6) <input type="checkbox"/> Other: _____ .

## **DETAILED ACTION**

1. This office action is in response to the amendment filed 21 January 2009. Claims 1-2, 3-7, 9-17, 20-31, 34-44, 46-53 and 55-58 are pending. Claims 3, 8, 18, 19, 32, 33, 45, 54 and 59-62 have been cancelled.

### ***Response to Arguments***

2. Applicant's arguments filed 21 January 2009 have been fully considered but they are not persuasive.

On pages 13 and 14 of the response the applicant argues that Shaw does not teach the previous limitation of claim 3 now incorporated into the independent claims that recites: "wherein the signal checking unit checks whether the identified input signal is abnormal by one of decoding the identified input signal and sensing whether an input signal cable is connected to the display device". Specifically the applicant states that the examiner's position that determining whether an input signal is or is not being received is not sensing whether an input cable is connected to the display device. The examiner respectfully disagrees. There is nothing specific in the claims preventing the examiner's interpretation, and further, the applicant's specification does not explain how an input cable is sensed to be connected but rather only discusses a sync signal being detected and broadly states that it is "sensed" that a cable is connected or not. Thus, as explained by the examiner in the rejection, if a cable is not plugged in then the sync

signal will not be present and the signal will be “abnormal” and thus it is “sensed” that a cable is not plugged in as required by the claims. Just because there are other reasons why the signal will be determined “abnormal” doesn’t mean that a determination of an abnormal signal when a cable is unplugged doesn’t occur. There’s no limitation in the claims stating that this is the only way to determine an abnormal signal. Further, as pointed out by the applicant in the response, the limitation in the claims says “one of...decoding...sensing,,,”, and thus since Shaw discloses decoding the sync signal from the input signal and then using this to determine it as being abnormal, then Shaw satisfies the first requirement of the claim anyways and doesn’t actually need to teach that a cable being plugged in is sensed or not. Regardless, Shaw discloses BOTH of these two features even though it is not required by the claims, and thus the references teach all of the limitations of the claims.

The applicant continues in the remaining parts of the response to recite claim limitations and then states that the references do not teach them, however, as stated in the rejection found below the references do teach the claim limitations and thus the rejections are proper.

### ***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

5. Claims 1-2, 3-7, 9-17, 22-31, 36-44, 46-53 and 55-58 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shaw et al. (US 5,276,436) in view of Sakuda et al. (US 5,886,545).

***Regarding claim 1***, Shaw et al. disclose a display device (Figure 3) comprising:  
a signal identifying unit that receives an input signal and identifies the type of the input signal (Figure 3 shows analog multiplex unit 34, which receives an input signal.);  
a signal checking unit checking whether the identified input signal is abnormal (Figures 3 and 6 and column 9, lines 48-53 explain that the microprocessor 36 checks the received input signal from the multiplex unit to determine if there is a horizontal synchronizing signal present or not, where no synchronizing signal means that the input signal is “abnormal.”); and  
a signal changing unit that switches from the checked input signal to a next input signal to be checked so that the signal checking unit checks whether the next input

signal is abnormal, if the identified input signal is determined to be abnormal (Figure 3 and 6 and column 9, lines 53-64 explain that the microprocessor tells the analog multiplex unit 34 to switch to a next input signal to be checked if no synchronizing signal is present, i.e. if it is abnormal, and then the next input signal will be checked for the synchronizing signals to see whether that signal is "abnormal".),

wherein the signal checking unit checks whether the identified input signal is abnormal by one of decoding the identified input signal and sensing whether an input signal cable is connected to the display device (Column 9, lines 53-64 explain that the microprocessor tells the analog multiplex unit 34 to switch to a next input signal to be checked if no synchronizing signal is present. If there is no cable connected, there will be no synchronizing signal and thus the checking unit will sense whether or not there is a cable connected.).

Shaw et al. fail to teach of a data setting unit that sets data corresponding to a user input regarding the received input signal, the set data representing how to check the identified input signal, and that the signal changing unit switches based on the set data.

Sakuda et al. disclose a display device comprising a data setting unit that sets data corresponding to a user input regarding a received input signal, the set data representing how to check the identified input signal (Figure 1, where column 3, lines 16-20 and 35 to column 4, line 11 explain that there is a switch 9, i.e. a data setting unit, that sets the priority level of the port, i.e. data, and

this priority data represents which order to check the ports, i.e. it represents how to check the signals.), and

a signal changing unit that switches from one signal to a next based on the set data (Figure 1 and column 3, line 35 to column 4, line 3 explain that the ports are switched to check signals based upon the priority data set by the user.).

Therefore, it would have been obvious to "one of ordinary skill" in the art at the time the invention was made to use the teaching of Sakuda et al. to allow a user to set priority to the ports taught by Shaw et al. in order to allow the user control over the checking of the ports.

***Regarding claim 2***, Shaw et al. and Sakuda et al. et al. disclose the display device of claim 1.

Sakuda et al. also disclose wherein the signal identifying unit identifies whether the received input signal is one of a D-sub analog signal, a DVI analog signal, a DVI digital signal, and a VIDEO signal (Figure 1 shows a D-sub signal.).

***Regarding claim 4***, Shaw et al. and Sakuda et al. disclose the display device of claim 1.

Sakuda et al. discloses a data setting unit that sets one of a number of times the identified input signal is checked, a time required to check the identified input signal, and a position of the identified input signal to be checked within a sequence of identified input signals to be checked (As explained above, the user can set a priority to the ports

and thus determines a position of the ports, i.e. signals, to be checked within a sequence of the ports.),

wherein if the signal checking unit has not checked one of the number of set times whether the identified input signal is abnormal and has not checked for the period of set time whether the identified input signal is abnormal, the signal checking unit continues checking whether the identified input signal is abnormal (Since the option is given above and the examiner chose the “position” option, then Sakuda does not need to teach this limitations because the “number of times” and the “time required to check” limitations do not exist in the claim after the “position” option is chosen.).

***Regarding claim 5***, Shaw et al. and Sakuda et al. disclose the display device of claim 4.

Sakuda et al. also disclose the display device further comprising a signal controlling unit that checks the position of the checked input signal within the sequence of identified input signals to be checked to determine which identified input signal is to be checked after the checked input signal, wherein the signal changing unit switches from the checked input signal to the determined input signal so that the signal checking unit checks whether the determined input signal is abnormal (As explained above, the position is checked based upon the user priority setting, and thus one port is checked first and then if the signal is abnormal then the next port is checked.).

***Regarding claim 6***, this claim is rejected under the same rationale as claim 1.

***Regarding claim 7***, this claim is rejected under the same rationale as claim 2.

***Regarding claim 9***, this claim is rejected under the same rationale as claim 4.

***Regarding claim 10***, this claim is rejected under the same rationale as claim 5.

***Regarding claim 11***, please refer to the rejection of claim 1, and furthermore Shaw et al. also disclose wherein if the checked input signal is normal, the signal continues being displayed by the display device and if the checked input signal is abnormal, the signal stops being displayed by the display device (Column 9, lines 43-64 explain that if the signal is "abnormal", then the next input signal is checked, which means that the signal will be stopped from being displayed by the display device. Column 6, lines 21-51 and column 9, line 65 through column 10, line 19 state that when a signal is determined to be "normal" then the signal is passed and displayed on the display device.).

***Regarding claim 12***, Shaw et al. and Sakuda et al. disclose the display device of claim 11.

Shaw et al. also disclose wherein the identified input signal and the next input signal are abnormal if cables carrying the signals are not connected to the display device (Column 9, lines 53-64 explain that the microprocessor tells the analog multiplex

unit 34 to switch to a next input signal to be checked if no synchronizing signal is present. If there is no cable connected, there will be no synchronizing signal and thus the checking unit will sense there is not a cable connected.).

***Regarding claim 13***, Shaw et al. and Sakuda et al. disclose the display device of claim 11.

Sakuda et al. also disclose wherein the identified input signal and the next input signal are abnormal if H-sync and V-sync patterns associated with the signals are abnormal (Column 3, line 50 to column 4, line 3.).

***Regarding claim 14***, Shaw et al. and Sakuda et al. disclose the display device of claim 11.

Sakuda et al. also disclose wherein the signal identifying unit identifies whether the received input signal is a D-sub analog signal (Figure 1).

***Regarding claim 15***, Shaw et al. and Sakuda et al. disclose the display device of claim 11.

Shaw et al. and Sakuda et al. fail to explicitly teach wherein the signal identifying unit identifies whether the received input signal is a DVI analog signal, however, DVI analog signals are well known in the art, therefore it would have been obvious to “one of ordinary skill” in the art at the time the invention was made that one of the signals identified by Shaw et al. and Sugihara et al. could be a DVI analog signal.

***Regarding claim 16***, Shaw et al. and Sakuda et al. disclose the display device of claim 11.

Shaw et al. and Sakuda et al. fail to explicitly teach wherein the signal identifying unit identifies whether the received input signal is a DVI digital signal, however, DVI digital signals are well known in the art, therefore it would have been obvious to “one of ordinary skill” in the art at the time the invention was made that one of the signals identified by Shaw et al. and Sugihara et al. could be a DVI digital signal.

***Regarding claim 17***, Shaw et al. and Sakuda et al. disclose the display device of claim 1.

Sakuda et al. also disclose wherein the signal identifying unit identifies whether the received input signal is a VIDEO signal (Figure 1 shows signals from a BNC connector, which are video signals.).

***Regarding claim 22***, Shaw et al. and Sakuda et al. disclose the display device of claim 11.

Sakuda et al. also disclose the display device further comprising a data setting unit that sets the position of the identified input signal to be checked within a sequence of identified input signals to be checked (As explained above, the user can set a priority to the ports and thus determines a position of the ports, i.e. signals, to be checked within a sequence of the ports.).

***Regarding claim 23***, Shaw et al. and Sakuda et al. disclose the display device of claim 22.

Sakuda et al. also disclose the display device further comprising a signal controlling unit that checks the position of the checked input signal within the sequence of identified input signals to be checked to determine which identified input signal is to be checked after the checked input signal, wherein the signal changing unit switches from the checked input signal to the determined input signal so that the signal checking unit can check whether the determined input signal is abnormal (As explained above, the position is checked based upon the user priority setting, and thus one port is checked first and then if the signal is abnormal then the next port is checked.).

***Regarding claim 24***, Shaw et al. and Sakuda et al. disclose the display device of claim 11.

Sakuda et al. also disclose the display device further comprising a menu from which a user determines the identified input signal is to be checked and a checking order (Column 3, lines 15-20 explain that there can be an on-screen display, i.e. a menu, for the user to select the priority, i.e. order to checking the signals.).

***Regarding claim 25***, this claim is rejected under the same rationale as claim 11.

***Regarding claim 26***, this claim is rejected under the same rationale as claim 14.

***Regarding claim 27***, this claim is rejected under the same rationale as claim 15.

***Regarding claim 28***, this claim is rejected under the same rationale as claim 16.

***Regarding claim 29***, this claim is rejected under the same rationale as claim 17.

***Regarding claim 30***, this claim is rejected under the same rationale as claim 12.

***Regarding claim 31***, this claim is rejected under the same rationale as claim 13.

***Regarding claim 36***, this claim is rejected under the same rationale as claim 22.

***Regarding claim 37***, this claim is rejected under the same rationale as claim 23.

***Regarding claim 38***, this claim is rejected under the same rationale as claim 24.

***Regarding claim 39***, this claim is rejected under the same rationale as claim 25.

***Regarding claim 40***, please refer to the rejection of claim 1, and furthermore Sakuda et al. also disclose wherein at least one of the input ports has priority in an order of checking by the signal changing unit as compared to another input port,

wherein at least one of the input ports can be set to have a priority in an order of checking by the signal checking unit as compared to another input port (Figure 1, where column 3, lines 16-20 and 35 to column 4, line 11 explain that a user sets the priority level of the ports, and this priority data represents which order to check the ports, and thus one port can be set by a user to have a priority in the order of checking over another port.).

***Regarding claim 41***, Shaw et al. and Sakuda et al. disclose the displaying device of claim 40.

Sakuda et al. also disclose wherein the order of checking of the input port is selected among a plurality of checking orders (Since each port can be selected to have a priority then the order is one of a plurality.).

***Regarding claim 42***, Shaw et al. and Sakuda et al. disclose the displaying device of claim 41.

Sakuda et al. also disclose wherein the checking order is set by the user (As explained above, the user controls the switch 9 or on-screen display menu to select the priority of the ports.).

***Regarding claim 43***, Shaw et al. and Sakuda et al. disclose the displaying device of claim 42.

Sakuda et al. also disclose wherein a menu is provided on a screen of the displaying device to set the checking order (Column 3, lines 17-20).

***Regarding claim 44***, this claim is rejected under the same rationale as claim 17.

***Regarding claim 46***, Shaw et al. and Sakuda et al. disclose the displaying device of claim 40.

Shaw et al. also disclose wherein the displaying device is capable of displaying a computer signal (Figure 3, element 21 is a computer, which are capable of display on the active matrix panel 16.).

***Regarding claims 47 and 48***, please refer to the rejection of claim 1, and furthermore Sakuda et al. also disclose an analog input port for receiving an analog signal and a digital input port for receiving a digital signal (Figure 1 shows that there is a Dsub port and a BNC port, where the Dsub port is for receiving an analog signal and the BNC connector is for receiving both analog and digital signals, meaning that it is for receiving digital signals.), and that the switching occurs between the analog and digital ports (Figure 1 and column 3, lines 16-20 and 35 to column 4, line 11.).

***Regarding claim 49***, this claim is rejected under the same rationale as claim 40.

***Regarding claim 50***, this claim is rejected under the same rationale as claim 14.

***Regarding claim 51***, this claim is rejected under the same rationale as claim 15.

***Regarding claim 52***, this claim is rejected under the same rationale as claim 16.

***Regarding claim 53***, this claim is rejected under the same rationale as claim 17.

***Regarding claim 55***, this claim is rejected under the same rationale as claim 13.

***Regarding claim 56***, this claim is rejected under the same rationale as claim 41.

***Regarding claim 57***, this claim is rejected under the same rationale as claim 42.

***Regarding claim 58***, this claim is rejected under the same rationale as claim 43.

6. Claims 20-21 and 34-35 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shaw et al. (US 5,276,436) in view of Sakuda et al. (US 5,886,545) and further in view of Yamashita et al. (US 5,808,693).

***Regarding claim 20***, Shaw et al. and Sakuda et al. disclose the display device of claim 11.

Shaw et al. and Sakuda et al. fail to teach the display device further comprising a data setting unit that sets the number of times the identified input signal is checked, wherein if the signal checking unit has not checked the number of set times, the signal checking unit continues the checking.

Yamashita et al. disclose a display device comprising a data setting unit that sets the number of times an identified input signal is checked, wherein if a signal checking unit has not checked the number of set times, the signal checking unit continues the checking (As shown in Figure 2 the number of times the input signal is checked is 1, so when it hasn't been checked it is checked and after it is checked once it moves on to the next input signal.).

Therefore it would have been obvious to "one of ordinary skill" in the art at the time the invention was made to use the teachings of Yamashita et al. in the display device taught by the combination of Shaw et al. and Sakuda et al. in order to allow for enough time to check whether the input signal is the correct input signal or not.

***Regarding claim 21***, Shaw et al. and Sakuda et al. disclose the display device of claim 11.

Shaw et al. and Sakuda et al. fail to teach the display device further comprising a data setting unit that sets the time required to check the identified input signal, wherein if the signal checking unit has not checked the identified input signal for the set period of time, the signal checking unit continues checking whether the identified signal is abnormal.

Yamashita et al. discloses a display device comprising a data setting unit that sets the time required to check the identified input signal (Figure 2 shows that a timer is set for checking the identified input signal),

wherein if the signal checking unit has not checked the identified input signal for the set period of time, the signal checking unit continues checking whether the identified signal is abnormal (Column 6, lines 1-32 and Figures 2 and 3 shows that the process repeats for checking the input signal abnormality.).

Therefore it would have been obvious to “one of ordinary skill” in the art at the time the invention was made to use the teachings of Yamashita et al. in the display device taught by the combination of Shaw et al. and Sakuda et al. in order to allow for enough time to check whether the input signal is the correct input signal or not.

***Regarding claim 34***, this claim is rejected under the same rationale as claim 20.

***Regarding claim 35***, this claim is rejected under the same rationale as claim 21.

### ***Conclusion***

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to STEPHEN G. SHERMAN whose telephone number is (571)272-2941. The examiner can normally be reached on M-F, 8:00 a.m. - 4:30 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amr Awad can be reached on (571) 272-7764. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Stephen G Sherman/  
Examiner, Art Unit 2629

/Amr Awad/  
Supervisory Patent Examiner, Art Unit 2629

24 March 2009